

### IN THE SPECIFICATION

Please replace the paragraph starting on page 9, line 14 with the following paragraph:

Referring to FIG. 6, a process (or method) 200 for implementing a precise tuning operation of the present invention is shown. The process 200 may be configured to perform a first phase of calibration. The process 200 may be configured to center the filter 106. The process 200 generally comprises a state 202, a state 204, a state 206, a state 208, a decision state 210, a state 212 and a state 214. In the state 202, the digitally-switched capacitor array [204] 160 may be initially programmed to minimum capacitance value. In the state 204, a single fixed-frequency RF sinusoidal test signal may be injected into the filter 106 at the filter input. In the state 206, the rectifier circuit 124, coupled to the A/D converter 126 may directly measure amplitude of the post-filter signal. The output of the ADC 126 may be recorded at the state 208. In the state 210, the measurement (e.g., signal injection, rectification, and A/D conversion) may be done multiple times. The state 212 may average the results from the state 210 to minimize the impact of thermal noise. The state 214 may step the capacitor array to the next value. The process is repeated as needed.